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Karunya INSTITUTE OF TECHNOLOGY AND SCIENCES

(Declared as Deemed to be University under Sec.3 of the UGC Act, 1956)

MoE, UGC & AICTE Approved

NAAC A++ Accredited

Recruitment of Senior Research Fellows – Call for Applications

Ref: File No. EE-9/2/2021-R&D-E, dated May 22, 2023

Applications are invited from interested and eligible candidates to fill Two Senior Research Fellow positions for the project titled “**Configurable and Scalable IoT SoC for Automobile applications**” funded by the Ministry of Electronics & Information Technology, R&D in Electronics Group under Chips to Startup (C2S) Programme.

Name of Implementing Agencies : Karunya Institute of Technology and Sciences (KITS), Coimbatore

Project Duration : 5 Years

Details of Vacancies:

Postilion and Vacancy	Principal Investigator & Place of Work	Essential Qualification
Senior Project Associate - (Emoluments: 45,000/- PM as per MeitY norms for 5 years)	Dr. G. Manoj Assistant Professor, Department of ECE Email: manojpillai@karunya.edu Karunya Institute of Technology and Sciences, Coimbatore, Tamil Nadu	Post Graduate Degree in Professional Course (M. E / M. Tech in Applied Electronics/Communication Systems/ VLSI Design/ Nanotechnology/ Embedded Systems) with two years of research experience
Project Associate (Emoluments: 40,000/- PM as per MeitY norms for 5 years)		

Desirable Experience:

In addition to the above essential qualification, it is necessary that the candidates should have the following research experience and skills.

- Identify significant Emerging technologies that lead in analysis of their impacts.
- Physical back end including Formal Verification, Timing Closure, ECOs, Static Timing Analysis, Power Analysis, Power Analysis tools, Library files.
- Experience with common serial protocols such as I2C, SPI etc.,
- Expert at simulation analysis of analog behavior including a deep understanding of basic (R/L/C/Tr) element fabrication process, simulation models and model accuracy.
- Digital / μ Arch design, verification based on SV/UVM
- SPICE simulator types with deep knowledge of tradeoffs such as power modeling, SPICE and fast SPICE, noise, transient, and Monte Carlo Simulations
- Full physical verification flow and tools

General Terms and Conditions:

- Based on the specialization and depending on the suitability to the appropriate component of the Principal Investigator's (PI) designated objectives, interested and eligible candidates can mail their resumes to divya_deepam@karunya.edu or bellamary@karunya.edu not later than **20th June 2023**.

- The interview date will be communicated to the eligible candidates through email.
- The position is temporary and renewable each year subject to satisfactory performance for a maximum of two years.
- Original documents of age proof/certificates/degrees/mark sheet, evidence of two years research experience, list of publications and other testimonials must be presented at the time of interview.
- The candidate selected for this position can also apply to the regular Ph D program offered at respective Institutions, provided, he/she satisfies the eligibility.

The final selection would be notified within 5 working days of the interview date by email to the concerned candidate. Candidates who are already employed must produce relieving certificate from their employers, if selected.

Co-Principal Investigators



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